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We stress that the fault tolerance requirements and timing constraints are not orthogonal issues as they appear to be, and hence any viable approach must be an integrated one. Fault tolerance in a real-time system implies that the system is able to deliver correct results in a timely manner even in the presence of faults.

### **Fault Tolerant and Fault Testable Hardware Design (1985)**

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ii Candidate's Declaration I hereby declare that the work, which is being presented in the Dissertation, entitled " A New Gate for Optimal Fault Tolerant & Testable Reversible Sequential Circuit Design " in partial fulfillment of "Master of Technology" with specialization in Computer Science and

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